

ABSTRACT OF THE DISCLOSURE

A semiconductor integrated circuit having one or more functional circuit blocks and executing a set of instructions is configured so as to change the operating frequency or halt operation of said one or more functional circuit blocks for each instruction or execution cycle. Another semiconductor integrated circuit, having a plurality of internal or external memory blocks or an internal or external single memory block that can be dealt with as a plurality of logical memory blocks and executing a set of instructions, is configured so as to change the operating frequency according to the performance of the memory block for each instruction or execution cycle so that the operating speed during data access time in execution cycle can be changed.